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APPLICATION NO.	FILING DATE	FIRST NAMED INVI	ENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/258,961	03/01/1999	TONGBI JIANG		98-0645.1	4605	
7590 05/28/2004 STEPHEN A GRATTON 2764 SOUTH BRAUN WAY		4			EXAMINER PAREKH, NITIN	
LAKEWOOD,				ART UNIT	PAPER NUMBER	
	•			2811		
			e*	DATE MAILED: 05/28/2004	-	

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)	
	Office Action Comment	09/258,961	JIANG ET AL.	
	Office Action Summary	Examiner	Art Unit	
Resignation		Mull Farekii	2811 The second will be the second with the se	
	The MAILING DATE of this communication app Peri d for Reply	pears on the cover sheet with the	correspond nce address	
	A SHORTENED STATUTORY PERIOD FOR REPL THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period of the period for reply within the set or extended period for reply will, by statute any reply received by the Office later than three months after the mailing	136(a). In no event, however, may a reply be till by within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	mely filed ys will be considered timely. the mailing date of this communication. ED (35 U.S.C. & 133).	
	earned patent term adjustment. See 37 CFR 1.704(b). Status			
	<u> </u>	1		
	1) Responsive to communication(s) filed on 29 M		,	
	T	action is non-final.		
	3) Since this application is in condition for allowal			
•	closed in accordance with the practice under E	Ex parte Quayle, 1935 C.D. 11, 4	53 O.G. 213.	
	Disposition of Claims	•		
	4)⊠ Claim(s) <u>24-36</u> is/are pending in the application	_		
	4a) Of the above claim(s) is/are withdraw			
	5) Claim(s) is/are allowed.	will from consideration.		
•	l			
	6)⊠ Claim(s) <u>24-26 and 34-36</u> is/are rejected.	•		
	7) Claim(s) <u>27-33</u> is/are objected to.		•	
	8) Claim(s) are subject to restriction and/o	r election requirement.		
*	Application Papers			
	9)☐ The specification is objected to by the Examine	ır.		
	10)⊠ The drawing(s) filed on <u>03-01-99</u> is/are: a)⊠ a		ne Examiner	
	Applicant may not request that any objection to the			
*	Replacement drawing sheet(s) including the correct			
	11) The oath or declaration is objected to by the Ex			
	Pri rity under 35 U.S.C. § 119		•	
	12) Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a))-(d) or (f).	
	a) ☐ All b) ☐ Some * c) ☐ None of:			
	1. Certified copies of the priority documents	s have been received.	,	
	2. Certified copies of the priority documents	s have been received in Applicati	on No	
	Copies of the certified copies of the prior	ity documents have been receive	ed in this National Stage	
	application from the International Bureau	·	•	
	* See the attached detailed Office action for a list	of the certified copies not receive	ed.	
		;		
	Attachment(s)			
	1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO 413)	
İ	2) Notice of Praftsperson's Patent Drawing Review (PTO-948)	4) [_] Interview Summary Paper No(s)/Mail Da		
:	3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)	5) D Notice of Informal P	atent Application (PTO-152)	
	Paper No(s)/Mail Date	6) Other:	•	
	J.S. Patent and Trademark Office	tion Comment	D. (D.)	

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DETAILED ACTION

Request for Continued Examination

- 1. A request for continued examination (RCE) under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR 1.114. Applicant's submission filed on 03/29/04 has been entered. An action on the RCE follows.
- 2. The amendment filed on 03/29/2004 has been entered.

Claim Objections

- 3. Claims 27 and 30 are objected to because of the following informalities:
- A. Claim 27, line 20 and claim 30, line 23: Insert --- "a top surface of" --- between "covering" and "the".

Appropriate correction is required.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and

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the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

5. Claims 24 and 25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Lee et al. (US Pat. 5796586).

Regarding claim 24, Lee et al. disclose a semiconductor package comprising:

- a substrate (16 in Fig. 1A-1C) comprising a first/bottom and second/top surfaces (not numerically referenced- see Fig. 1A)
- a semiconductor die (20 in Fig. 1A) having a first outline (not numerically referenced- see the die 20 having the outline defining the die dimensions on the die attach area in the central portion in Fig. 1C) and a face being bonded directly to the second surface
- a first solder mask (14A in Fig. 1A) on the first/bottom surface of the substrate
- a second solder mask (18 in Fig. 1A) covering the second/top surface of the substrate except in a die attach area defined by an opening through the second solder mask having a second outline (see the outline/hatched area 18 in Fig. 1C) corresponding to or slightly larger than the first outline
- an adhesive layer (not numerically referenced in Fig. 1A; see Col. 6, lines 32-35) between the face and the die attach area bonding the die directly to the second/top surface of the substrate, and

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 an encapsulating material/epoxy resin (25 in Fig. 1A) on the die and the second mask

(Fig. 1A-1C; Col. 1, line 34-Col. 2, line 65).

Lee et al. further teach the solder masks being made of a photoimageable material such as polyimide and being applied using suitable/patterning technique (Col. 5, line 18-45; Col. 7, line 65). Conventional patterning technique use steps of photo exposure and development of the photoimageable material to achieve the desired pattern/dimensions.

Furthermore, having the opening in the second mask comprising an exposed and developed portion do not distinguish over Lee et al., because only the final product/structure is relevant, not the process of forming the opening such as "exposing/developing", "laser etching" or "sputtering". Note that a "product by process" claim is directed to the product per se, no matter how actually made, In re Hirao, 190 USPQ 15 at 17 (footnote 3). See also In re Brown, 173 USPQ 685; In re Luck, 177 USPQ 523; In re Fessmann, 180 USPQ 324; In re Avery, 186 USPQ 161; In re Wertheim, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); and In re Marrosi et al., 218 USPQ 289, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims

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or not . Note that applicant has the burden of proof in such cases, as the above case law makes clear. See also MPEP 706.03(e).

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Regarding claim 25, Lee et al. substantially teach the entire claimed structure as applied to claim 24 above, wherein Lee et al. further teach the second outline being only slightly larger than the first outline (see Fig. 1A-1C).

6. Claim 26 is rejected under 35 U.S.C. 103(a) as being unpatentable over Lee et al. (US Pat. 5796586) in view of Hoffman et al. (US Pat. 5360942).

Regarding claim 26, Lee et al. teach substantially the entire claimed structure as applied to the claims 24 and 25 above, except the adhesive layer comprising a filled adhesive configured to transfer heat from the face to the second surface.

Lee et al. further teach in another embodiment, the die bonding structure having different adhesives including a conductive epoxy (Col. 6, line 45-50).

Hoffman et al. teach using a die attach/adhesive material comprising a filled epoxy (40 in Fig. 3) configured to provide improved heat transfer and/or electrical conductivity (Col. 3, line 60- Col. 4, line 2).

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to incorporate the adhesive layer comprising a filled adhesive configured to transfer heat from the face to the second surface as taught by Hoffman et

al. so that the adhesion, thermal performance and reliability for the package can be improved in Lee et al's package.

7. Claims 34 and 35 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art (APA) in view of Lee et al. (US Pat. 5796586).

Regarding claims 34 and 35, APA (Fig.1A and B; pages 2-4) discloses a semiconductor package comprising :

- a substrate (12 in Fig. 1A) comprising a first and second surfaces (24 and 22 respectively in Fig. 1A), a plurality of conductors/pads formed on the first surface and a bonding opening (26 in Fig. 1A) from the first surface to the second surface
- a semiconductor die (16 in Fig. 1A) having a first outline (not numerically referenced- see the die 20 having the outline defining the die dimensions on the die attach area in the central portion in Fig. 1C) and a face being aligned with the bonding opening and attached/bonded face down to the second surface on a die attach area using an adhesive (34 in Fig. 1A)
- first mask (20A in Fig. 1A) on the first surface of the substrate
- a second mask (20B in Fig. 1A) substantially covering a second surface of the substrate
- the adhesive layer between the die/face and the substrate in the die attach area to bond the die to the second mask and the substrate

- a plurality of wires (28 in Fig. 1A) placed through the bonding opening and wire bonded to the die and being in electrical communication with the respective conductors
- an encapsulating material/epoxy resin (38 in Fig. 1A) on the die and the second mask, and
- a glob top/polymer (40 in Fig. 1A) in the bonding opening encapsulating the wire.

APA further teaches the solder masks being made of a photoimageable material and being patterned using conventional exposure and development of the photoimageable material (see page 3; line 31- page 4, line 6).

APA fails to teach:

- a) the die being bonded directly to the second surface, the second surface having the second mask including an opening there through having a second outline corresponding to but only slightly larger than the first outline, and
- b) the adhesive layer bonding the die directly to the second surface.
- a) Lee et al. teach using a second mask having an opening through the mask with a second outline (see hatched mask area 218' with a second outline- Fig. 7; Col. 7, line 55) corresponding to or only slightly larger than the first outline (see the outline of the die 220 and the area 218' in Fig. 4 and Fig. 7) on the second surface so that the die

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is directly bonded to the second surface to provide an area with improved adhesion with the substrate (see abstract: lines 8-11; Fig. 7 and Fig. 1-6; Col. 1-8).

b) Lee et al. further teach the die bonding structure where the die is attached directly to the substrate surface using a variety of adhesives including the conductive/filler-based epoxy (not numerically referenced in Fig. 7; Col. 6, lines 30-50).

It would have been obvious to a person of ordinary skill in the art at the time invention was made to incorporate the die being bonded directly to the second surface, the second surface having the second mask including an opening there through having a second outline corresponding to but slightly larger than the first outline and the adhesive layer bonding the die directly to the second surface as taught by Lee et al. so that the adhesion, bonding to the substrate and the reliability of the package can be improved in the APA.

Regarding claim 34, having the opening in the second mask comprising an exposed and developed portion do not distinguish over APA and Lee et al., because only the final product/structure is relevant, not the process of forming the opening such as "exposing/developing", "laser etching" or "sputtering". Note that a "product by process" claim is directed to the product per se, no matter how actually made, In re Hirao, 190 USPQ 15 at 17 (footnote 3). See also In re Brown, 173 USPQ 685; In re Luck, 177

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USPQ 523; In re Fessmann, 180 USPQ 324; In re Avery, 186 USPQ 161; In re Wertheim, 191 USPQ 90 (209 USPQ 554 does not deal with this issue); and In re Marrosi et al., 218 USPQ 289, all of which make it clear that it is the patentability of the final product per se which must be determined in a "product by process" claim, and not the patentability of the process, and that an old or obvious product produced by a new method is not patentable as a product, whether claimed in "product by process" claims or not . Note that applicant has the burden of proof in such cases, as the above case law makes clear. See also MPEP 706.03(e).

8. Claim 36 is rejected under 35 U.S.C. 103(a) as being unpatentable over APA and Lee et al. (US Pat. 5796586) as applied to claim 34 above, and in further in view of Hoffman et al. (US Pat. 5360942).

Regarding claim 36, APA and Lee et al. teach substantially the entire claimed structure as applied to the claim 34 above, except the adhesive layer comprising a filled adhesive.

Lee et al. further teach in another embodiment, the die bonding structure having different adhesives including a conductive epoxy adhesive (Col. 6, line 45-50).

Hoffman et al. teach using a die attach/adhesive material comprising a filled epoxy/adhesive (40 in Fig. 3) configured to provide improved heat transfer and/or electrical conductivity (Col. 3, line 60- Col. 4, line 2).

It would have been obvious to a person of ordinary skill in the art at the time the invention was made to incorporate the adhesive layer comprising the filled adhesive configured to transfer heat from the face to the second surface as taught by Hoffman et al. so that the adhesion, thermal performance and reliability for the package can be improved in Lee et al. and APA's package.

Response to Arguments

8. Applicant's arguments filed on 03-29-04 with respect to claims 24-26, 34 and 35 have been addressed in the rejections above.

Allowable Subject Matter

9. Claims 27-33 would be allowable if rewritten to overcome the objections of the base claims set forth in this Office Action.

Reasons for Allowance

10. The following is an examiner's statement of reasons for allowance:

The references of record do not teach either singularly or in combination at least the limitations "a semiconductor die having a first outline and a face being bonded directly to the second surface", "a second mask on the second surface comprising a second opening having a second outline corresponding to a first outline defining an open die attach area on the second surface" and " a resin encapsulating the die and

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covering a top surface of the second mask" as recited in the independent claim 27; and "a semiconductor die on the die attach area having a face aligned with the bonding opening attached directly to the second surface", "a second mask on the second surface except in a die attach area defined by an opening in the second mask" and "a resin encapsulating the die and covering a top surface of the second mask" as recited in the independent claim 30 in an encapsulated package having a first and second solder masks on first and second surfaces of a substrate respectively and a die being bonded directly on the second surface of the substrate.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Parekh whose telephone number is 703-305-3410. The examiner can normally be reached on 09:00AM-05:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lee can be reached on 703-308-1690. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-306-3431.

hulardeh NITIN PAREKH

MINITARENT

PATENT EXAMINER

TECHNOLOGY CENTER 2800

NP